

IN THE SPECIFICATION:

Please replace the following paragraph on Page 3, lines 2 to line 7 as indicated.

On the other hand, in network Large Scale Integration (LSI) or image processing LSI, it is proposed that a logic circuit for the required data processing and an SDRAM macro for recording large amounts of data during data processing be embedded in one LSI chip. A memory controller is built into such an embedded memory logic LSI and this is used to control the SDRAM macro.